Test Report： ODLC-45-500

45W Constant Current Mode LED Driver

■ DESIGN VERIFY TEST

Output Function Test
Input Function Test
Protection Function Test
Component Stress Test

■ SAFETY & E.M.C. TEST

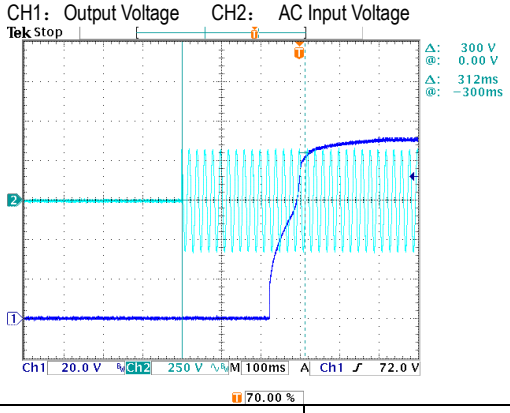
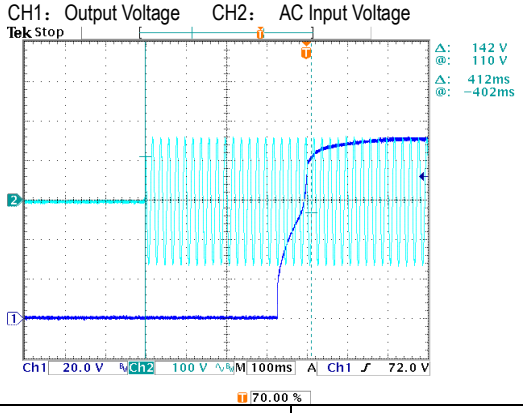
Safety Test
E.M.C. Test

■ RELIABILITY TEST

Environment Test

■ DESIGN VERIFY TEST

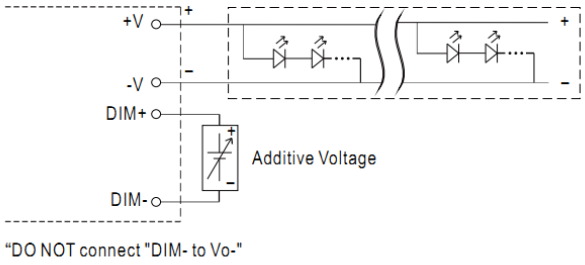
OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	CONSTANT CURRENT REGION	54V~90V	I/P： 230VAC O/P：LED MODE Ta：25℃	20V~95V
2	CURRENT RIPPLE	5% max@rated current	I/P： 230VAC O/P：FULL/MIN LOAD Ta：25℃	3.95%
3	CURRENT TOLERANCE	±7%	I/P： 230VAC O/P：FULL/MIN LOAD Ta：25℃	±1.0%
4	OPEN CIRCUIT VOLTAGE (max)	115V	I/P： 230VAC O/P：NO LOAD Ta：25℃	110.11V
5	OVER/UNDERSHOOT TEST	<±5 %	I/P： 230VAC O/P：FULL LOAD Ta：25℃	<5 %
6	SET UP TIME	500ms/230VAC 1200ms/115VAC	I/P： 230 VAC I/P： 115 VAC O/P：FULL LOAD Ta：25℃	312ms/230VAC 412ms/115VAC
	INPUT=230VAC/50HZ @ FULL LOAD CH1：Output Voltage CH2： AC Input Voltage 		INPUT=115VAC/60HZ @ FULL LOAD CH1：Output Voltage CH2： AC Input Voltage 	
7	AUXILIARY DC OUTPUT (A-Type only)	Nominal 12V（deviation 11.4~12.6） @50mA	I/P: 230 VAC O/P:FULL LOAD	11.98V

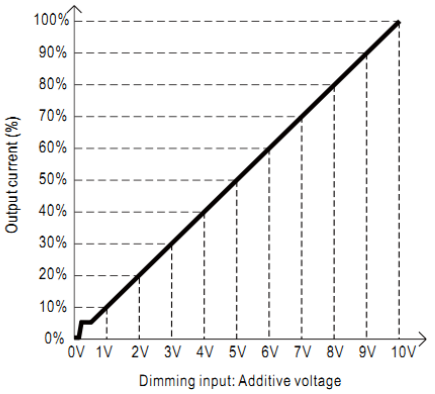
8 DIMMING TEST(For Blank -Type

- Output constant current level can be adjusted by applying one of the two methodologies between DIM+ and DIM-:
  0 ~ 10Vdc, or 10V PWM signal.
- Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers.

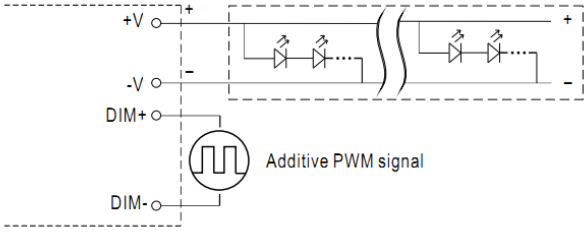
◎ Applying additive 0 ~ 10VDC



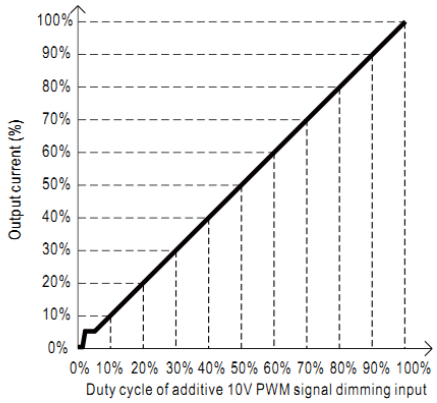
"DO NOT connect "DIM- to Vo-"



◎ Applying additive 10V PWM signal (frequency range 300Hz ~ 3KHz):



"DO NOT connect "DIM- to Vo-"



Note : 1. Min. dimming level is about 8% and the output current is not defined when 0%< Iout<8%.
2. The output current could drop down to 0% when dimming input is about 0Vdc or 10V PWM signal with 0% duty cycle.

I/P: 230 VAC
O/P: DIMMING TEST
Ta: 25℃

	V	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V
1	Output Current	0A	0.040	0.099	0.147	0.198	0.248	0.300	0.350	0.398	0.448	0.497
	%	0.00%	7.90%	19.74%	29.48%	39.66%	49.58%	59.90%	70.06%	79.56%	89.58%	99.30%
2	PWM(100Hz)	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
	Output Current	0A	0.043	0.101	0.151	0.200	0.250	0.299	0.348	0.398	0.447	0.491
	%	0.00%	8.58%	20.12%	30.24%	40.06%	50.04%	59.86%	69.66%	79.56%	89.34%	98.12%

TEST RESULT: OK

9 DALI DIMMING OPERATION (primary side; for DA-Type)

※DALI Interface
·Apply DALI signal between DA+ and DA-.
·DALI protocol comprises 16 groups and 64 addresses.
·Firse step is fixed at 8% of output.

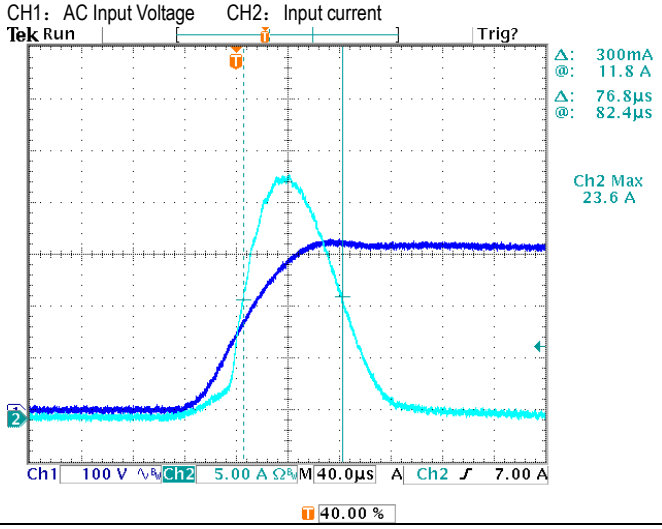
I/P: 230 VAC
O/P: DIMMING TEST
Ta: 25℃
TEST RESULT: OK

INPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	90VAC~295VAC	I/P：TESTING O/P：FULL LOAD Ta：25℃	87V~305V
			I/P： (1)LOW-LINE-3V=87 V HIGH-LINE+10V=305 V O/P：FULL/MIN LOAD ON：30 Sec OFF：30 Sec 10MIN (2)230VAC ON：0.5 Sec OFF：0.5 Sec 20MIN (POWER ON/OFF NO DAMAGE)	TEST：OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P： 90 VAC ~295 VAC O/P：FULL~MIN LOAD Ta：25℃	TEST：OK
3	AC CURRENT	0.6A/115VAC 0.4A/230VAC 0.3A/277VAC	I/P： 115 VAC I/P： 230 VAC I/P： 277 VAC O/P：FULL LOAD Ta：25℃	I =0.434A/ 115VAC I =0.219A/ 230VAC I =0.188A/ 277VAC
4	LEAKAGE CURRENT	< 0.75mA / 277VAC	I/P： 277 VAC O/P：NO LOAD Ta：25℃	L-CASE： 0.0027 mA N-CASE： 0.0027 mA
5	NO LOAD/STANDBY POWER CONSUMPTION	< 0.5W for Blank-Type < 1.2W for A-Type < 0.5W for DA-Type	I/P：230VAC O/P：NO LOAD Ta：25℃	0.468W for Blank-Type 0.568W for A-Type 0.453W for DA-Type
6	INRUSH CURRENT(Typ)	230V/ 30A Twidth =100 us measured at 50% Ipeak COLD START	I/P： 230 VAC O/P：FULL LOAD Ta：25℃	I =23.6A/ 230VAC Twidth =76.8us

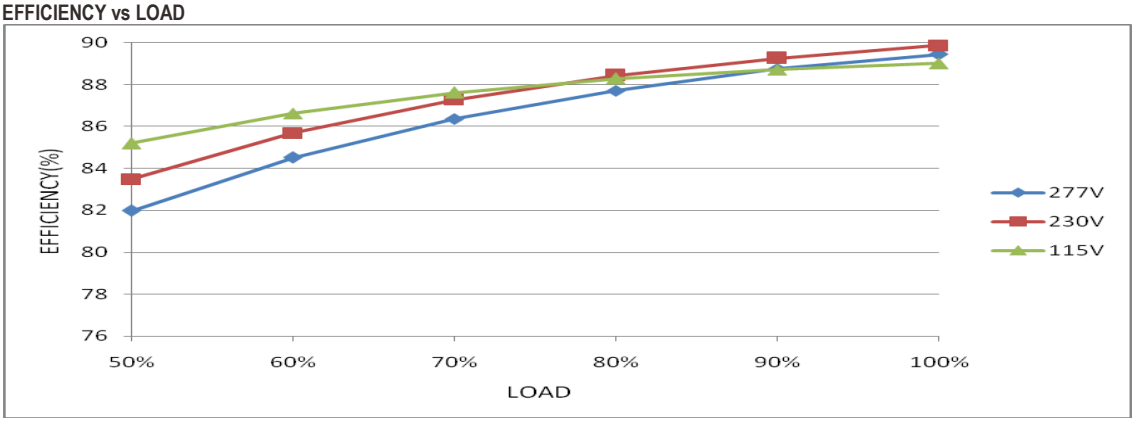
INPUT=230VAC/50HZ @ FULL LOAD

CH1：AC Input Voltage CH2：Input current



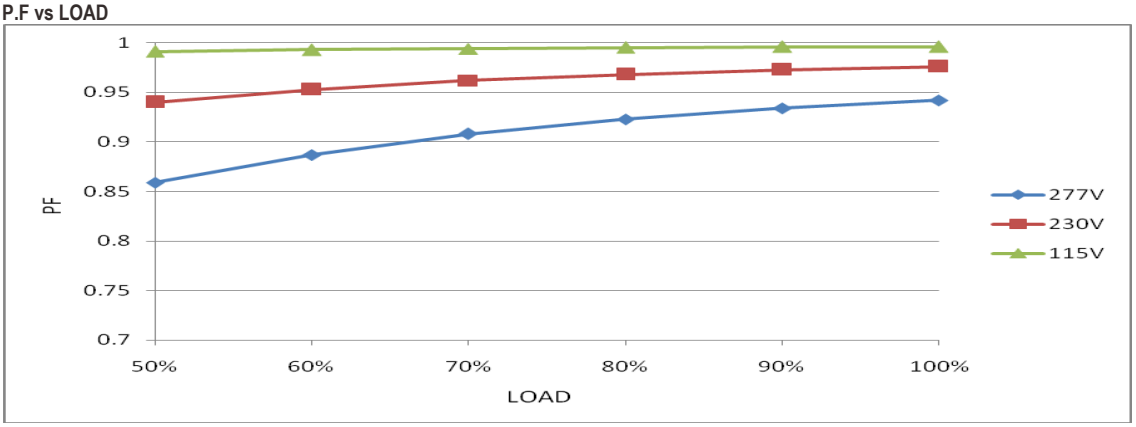
7	EFFICIENCY(Typ)	85%	I/P： 230VAC O/P：FULL LOAD Ta：25℃	89.87%

EFFICIENCY vs LOAD



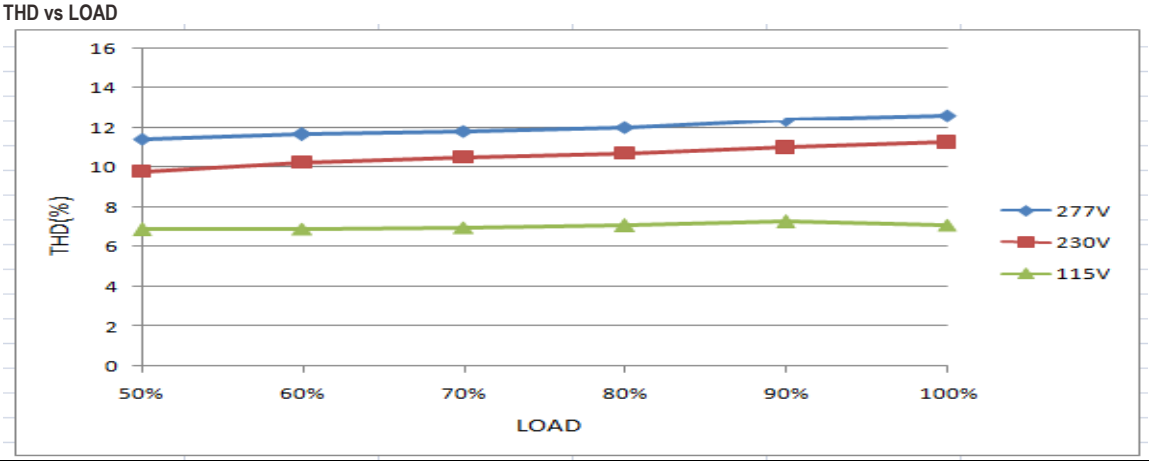
8	POWER FACTOR	0.95/ 115VAC 0.92/ 230VAC 0.90/ 277VAC	I/P： 115 VAC I/P： 230 VAC I/P： 277 VAC O/P：FULL LOAD Ta：25℃	PF=0.996/ 115VAC PF=0.976/ 230VAC PF=0.942/ 277VAC

P.F vs LOAD



9	TOTAL HARMONIC DISTORTION	THD＜20% （@load≥60%/115VAC，230VAC； @load≥75%/277VAC）	I/P：115 VAC/60% LOAD I/P：230 VAC/60% LOAD I/P：277 VAC/75% LOAD Ta：25℃	THD=6.89% @60% load /115VAC THD=9.80% @60% load /230VAC THD=11.85% @75% load /277VAC

THD vs LOAD



PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	SHORT CIRCUIT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P： 90VAC I/P： 295VAC O/P：FULL LOAD Ta：25℃	NO DAMAGE Hiccup mode，recovers automatically after fault condition is removed

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Power Transistor	Q 1 Rated 800V/9A	I/P：High-Line +3V =298V O/P： (1) Full Load Turn on (2) Output Short (3) Full load continue Ta：25℃	(1) 608V (2) 524V (3) 542V
2	O/P Diode (MOSFET)	D101 Rated 1000V/5A	I/P：High-Line +3V =298V O/P： (1) Full Load Turn on (2) Output Short (3) Full load continue Ta：25℃	(1) 808V (2) 796V (3) 804V
3	Control IC	U1 Rated 35V (MAX)	I/P：High-Line +3V =298V O/P：(1) FULL LOAD (2) Output Short (3)Low Line No Load Ta：25℃	(1) 17.9V (2) 17.9V (3) 17.9V
4	Clamp Diode	D 1 Rated 1000V/1A	I/P：High-Line +3V = 298V O/P: (1) Full Load input on/off (2) Output Short Ta：25℃	(1) 490V (2) 458V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P：3.75KVAC/min	I/P-O/P：4.2 KVAC/min Ta：25℃	I/P-O/P：2.487mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P： 500VDC>100MΩ	I/P-O/P： 500 VDC Ta：25℃/70% RH	I/P-O/P： ＞9999MΩ

E.M.C TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P：230 VAC/50HZ O/P：FULL/60% LOAD Ta：25℃	PASS
2	CONDUCTION	EN55015	I/P：230 VAC/50HZ O/P：FULL LOAD Ta：25℃	PASS Test by certified Lab
3	RADIATION	EN55015	I/P：230 VAC/50HZ O/P：FULL LOAD Ta：25℃	PASS Test by certified Lab
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR：8KV Contact：4KV	I/P：230 VAC/50HZ O/P：FULL LOAD Ta：25℃	PASS
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT：1KV	I/P： 230VAC/50HZ O/P：FULL LOAD Ta：25℃	PASS
6	SURGE	EN61000-4-5 LIGHT INDUSTRY L-N：1KV	I/P： 230VAC/50HZ O/P：FULL LOAD Ta：25℃	PASS
7	Test by certified Lab ＆ Test Report Prepare			

■ RELIABILITY TEST

ENVIRONMENT TEST

<table>
<tr><th>NO</th><th>TEST ITEM</th><th>SPECIFICATION</th><th>TEST CONDITION</th><th>RESULT</th></tr>
<tr><td>1</td><td>TEMPERATURE RISE TEST</td><td colspan="3">MODEL：ODLC-45-700
1. ROOM AMBIENT BURN-IN：2 HRS
I/P： 230VAC O/P： FULL LOAD Ta= 32.5℃
2. HIGH AMBIENT BURN-IN： 2 HRS
I/P： 230VAC O/P： FULL LOAD Ta= 53.7℃
<table>
<tr><th>NO</th><th>Position</th><th>ROOM AMBIENT Ta= 32.5 ℃</th><th>HIGH AMBIENT Ta=53.7 ℃</th></tr>
<tr><td>1</td><td>L1</td><td>69.7℃</td><td>91.0℃</td></tr>
<tr><td>2</td><td>BD1</td><td>66.5℃</td><td>87.2℃</td></tr>
<tr><td>3</td><td>D1</td><td>68.5℃</td><td>90.2℃</td></tr>
<tr><td>4</td><td>Q1</td><td>72.1℃</td><td>93.8℃</td></tr>
<tr><td>5</td><td>U1</td><td>66.2℃</td><td>87.1℃</td></tr>
<tr><td>6</td><td>T1</td><td>71.1℃</td><td>92.3℃</td></tr>
<tr><td>7</td><td>C16</td><td>65.5℃</td><td>86.7℃</td></tr>
<tr><td>8</td><td>D101</td><td>73.3℃</td><td>94.1℃</td></tr>
<tr><td>9</td><td>C201</td><td>63.5℃</td><td>84.5℃</td></tr>
<tr><td>10</td><td>Q100</td><td>62.4℃</td><td>83.6℃</td></tr>
<tr><td>11</td><td>L100</td><td>61.1℃</td><td>82.7℃</td></tr>
<tr><td>12</td><td>C106</td><td>59.9℃</td><td>80.9℃</td></tr>
<tr><td>13</td><td>C110</td><td>59.4℃</td><td>80.6℃</td></tr>
<tr><td>14</td><td>Tc</td><td>60.9℃</td><td>80.3℃</td></tr>
</table></td></tr>
<tr><td>2</td><td>LOW TEMPERATURE
TURN ON TEST</td><td>TURN ON AFTER 2 HOUR</td><td>I/P： 295VAC/90VAC
O/P：FULL/80% LOAD
Ta= -25℃</td><td>TEST：OK</td></tr>
<tr><td>3</td><td>HIGH HUMIDITY
HIGH TEMPERATURE
HIGH VOLTAGE
TURN ON TEST</td><td>AFTER 12 HOURS
IN CHAMBER ON
CONTROL 50 ℃
NO DAMAGE</td><td>I/P： 305VAC
O/P：FULL LOAD
Ta=50 ℃
HUMIDITY= 95 %R.H</td><td>TEST：OK</td></tr>
<tr><td>4</td><td>TEMPERATURE
COEFFICIENT</td><td>±0.03 %/℃(0~40℃)</td><td>I/P： 230 VAC
O/P：FULL LOAD</td><td>±0.001%/℃</td></tr>
<tr><td>5</td><td>STORAGE TEMPERATURE TEST</td><td colspan="2">1. Thermal shock Temperature： -45℃~ +85℃
2. Temperature change rate ： 25℃ / MIN
3. Dwell time low and high temperature ： 30 MIN/EACH
4. Total test cycle：5 CYCLE
5. Input/Output condition：AC OFF STATIC</td><td>TEST：OK</td></tr>
</table>

6	THERMAL SHOCK TEST	1. Thermal shock Temperature： Tcase=-25℃~ +45℃ 2. Temperature change rate ： 25℃ / MIN 3. Dwell time low and high temperature ： 30 MIN/EACH 4. Total test cycle：16 CYCLE 5. Input/Output condition： 230VAC/Full Load AC ON/OFF TEST AC on 3 sec/AC off 1 sec TEST	TEST：OK
7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform： Sine Wave (2) Frequency：10~500Hz (3) Sweep Time：10min/sweep cycle (4) Acceleration：2G (5) Test Time：60min in each axis (X.Y.Z) (6) Ta：25℃	TEST：OK
8	CAPACITOR LIFE CYCLE	ODLC-45-700：SUPPOSE C106 IS THE MOST CRITICAL COMPONENT (1) I/P： 230VAC O/P：FULL LOAD Ta= 25 ℃ LIFE TIME (2) I/P： 230VAC O/P：FULL LOAD Ta= 50 ℃ LIFE TIME (3) I/P： 230VAC O/P：75% LOAD Ta= 50 ℃ LIFE TIME (4) I/P： 230VAC O/P：MIN LOAD Ta= 50 ℃ LIFE TIME	(1) 555640 HRS (2) 84189 HRS (3) 99615 HRS (4) 107091 HRS
9	MTBF	Conducted by Parts Stress Analysis Prediction 3513.4K hrs min. Telcordia SR-332 (Bellcore) ; 340.8K hrs min. MIL-HDBK-217F (25℃)	
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure(Expected Life) ： 30,000 hours @ Tcase 85℃； 50,000 hours @ Tcase 75℃	

TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY